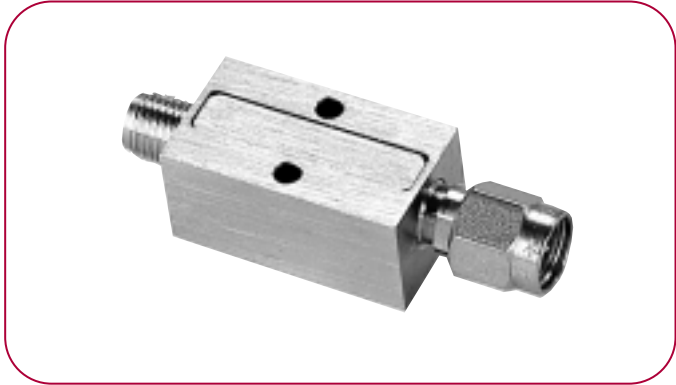


LIMITERS

FEATURES

- Optimized for lowest spike and flat leakage
- Designs suitable for sensitive PHEMT front ends
- Lowest insertion loss
- Power handling up to 60 dBm



MITEQ has extensive experience in the design and development of limiters for receiver protection. We specialize in developing limiters for critical radar receiver applications and EW applications. Designs based on PIN diodes, Schottky diodes, and combinations of the two semiconductor technologies are also available. In applications that require extremely fast response time MITEQ can offer a customized solution that meets your needs. We can provide limiters that safely handle pulsed power levels up to 60 dBm. These designs feature PIN diodes with plated heatsink that are eutectically-attached to a carrier for best possible thermal transfer. We offer limiters that feature Schottky diode arrays for the ultimate in insertion loss, flat output leakage and minimal spike leakage.

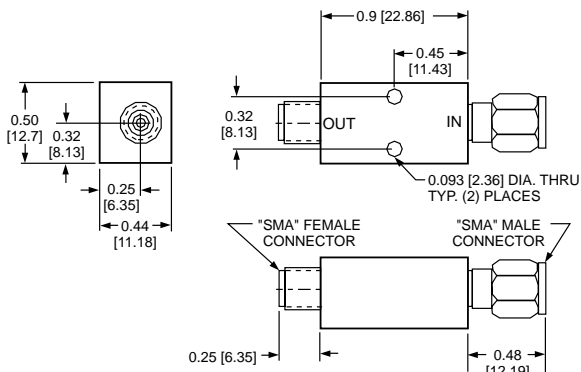
Model Number	Frequency Range (GHz)	Insertion Loss (dB, Max.)	VSWR In/Out (Max.)	RF Power (Max.)	Flat Leakage (dBm)	Spike Leakage (erg)	IP ₃ (dBm, Min.)	Linearity	Recovery Time (ns, Max.)	Outline
126159	0.5–1	0.7	1.3:1	1W CW, 400W peak, 1μs 0.1% DC	20	0.2	25	-50–0 dBm, w/in 0.1 dB	500	A
126160	1–2	0.7	1.3:1	1W CW, 400W peak, 1μs 0.1% DC	20	0.2	25	-50–0 dBm, w/in 0.1 dB	500	A
126156	0.5–2	1.5	1.5:1	100W peak, 10 μs pulse	15	0.2				B
129186	2.7–2.9	0.8	1.5:1	1W CW, 52 dBm peak, 80 μs pulse, rate: 1.4 ms	20	0.2			100	A

Additional bandwidths from 2–18 GHz are available. Consult factory for options.

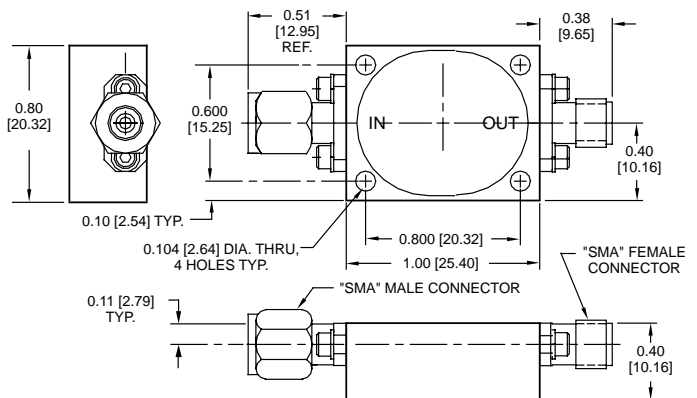
ENVIRONMENTAL CONDITIONS

Operating temperature 0 to 70°C
 Storage temperature -30 to +85°C

OUTLINE A



OUTLINE B



GENERAL NOTES:
 1. Dimensions shown in brackets [] are in millimeters.
 2. Tolerance as follows: .xx = ±0.01 [.xx = ±0.25], .xxx = ±0.005 [.xxx = ±0.13]