

NTE1396 Integrated Circuit 20W Bridge Booster for Car Radio

Description:

The NTE1396 is a Class B dual audio power amplifier specifically designed for car radio applications. Power booster amplifiers are easily designed using this device that provides a high current capability (up to 3.5A) and that can drive very low impedance loads obtaining an output power of more than 20W. The NTE1396 has high reliability and guarantees complete safety during operation due to protection against:

- Output DC and AC Short Circuit to GND or Across the Head
- Overrating Chip Temperature (+150°C)
- Polarity Inversion
- Load Dump Voltage Surge
- Fortuitous Open GND
- Very Inductive Loads

Absolute Maximum Ratings:

Operating Supply Voltage, V_S	18V
DC Supply Voltage, V_S	28V
Peak Supply Voltage (for 50ms), V_S	40V
Output Peak Current (Note 1), I_O	
Non-Repetitive ($t = 0.1\text{ms}$)	4.5A
Repetitive ($f > 10\text{Hz}$)	3.5A
Power Dissipation ($T_C = +60^\circ\text{C}$), P_{tot}	30W
Operating Junction Temperature Range, T_J	-40° to $+150^\circ\text{C}$
Storage Temperature Range, T_{stg}	-40° to $+150^\circ\text{C}$
Maximum Thermal Resistance, Junction-to-Case, R_{thJC}	3°C/W

Note 1. Max output current is internally limited.

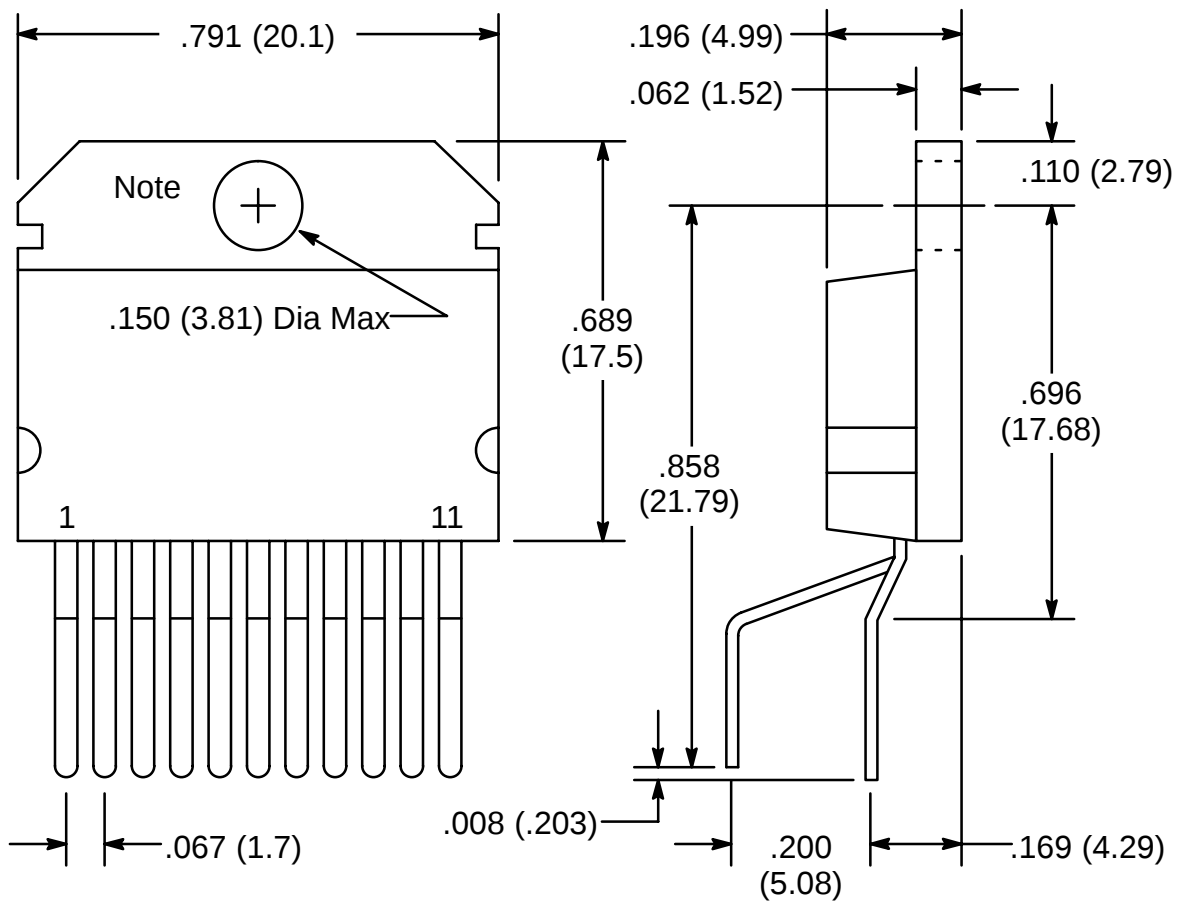
Electrical Characteristics: ($T_A = +25^{\circ}\text{C}$, $G_v = 50\text{dB}$, $R_{th(HS)} = 4^{\circ}\text{C/W}$, unless otherwise specified)

Parameter	Symbol	Test Conditions		Min	Typ	Max	Unit
Supply Voltage	V _S			8	–	18	V
Output Offset Voltage (Between Pin8 & Pin10)	V _{OS}	V _S = 14.4V		–	–	150	mV
		V _S = 13.2V		–	–	150	mV
Total Quiescent Drain Current	I _d	V _S = 14.4V, R _L = 4Ω		–	75	150	mA
		V _S = 13.2V, R _L = 3.2Ω		–	70	160	mA
Output Power	P _O	d = 10%, f = 1kHz	V _S = 14.4V, R _L = 4Ω	18	20	–	W
			V _S = 14.4V, R _L = 3.2Ω	20	22	–	W
			V _S = 13.2V, R _L = 3.2Ω	17	19	–	W
Distortion	d	f = 1kHz	V _S = 14.4V, R _L = 4Ω, P _O = 50mW to 15W	–	–	1	%
			V _S = 13.2V, R _L = 3.2Ω, P _O = 50mW to 13W	–	–	1	%
Input Sensitivity	V _i	R _L = 4Ω	f = 1kHz, P _O = 2W	–	9	–	mV
		R _L = 3.2Ω		–	8	–	mV
Input Resistance	R _i	f = 1kHz		70	–	–	kΩ
Low Frequency Roll Off (–3.2Ω)	f _L	R _L = 3.2Ω		–	–	40	Hz
High Frequency	f _H	R _L = 3.2Ω		20	–	–	kHz
Closed Loop Voltage Gain	G _v	f = 1kHz		–	50	–	dB
Total Input Noise Voltage	e _N	R _g = 10KΩ, Note 2		–	3	10	μV
Supply Voltage Rejection	SVR	R _g = 10KΩ , C ₄ = 10μF, f _{ripple} = 100Hz, V _{ripple} = 0.5V		45	55	–	dB
Efficiency	η	V _S = 14.4V , f = 1kHz	P _O = 20W, R _L = 4Ω	–	60	–	%
			P _O = 22W, R _L = 3.2Ω	–	60	–	%
		V _S = 13.2V, f = 1kHz, P _O = 19W, R _L = 3.2Ω		–	58	–	%
Thermal Shut–Down Case Temperature	T _{sd}	V _S = 14.4V, R _L = 4Ω, f = 1kHz, P _{tot} = 13W		100	110	–	°C
Output Voltage with One Side of the Speaker Shorted to GND	V _{OSH}	V _S = 14.4V, R _L = 4Ω		–	–	2	V
		V _S = 13.2V, R _L = 3.2V		–	–	2	V

Note 2. Bandwidth filter 22Hz to 22kHz

Pin Connection Diagram (Front View)

11	Bootstrap 1
10	Output 1
9	(+) V_S
8	Output 2
7	Bootstrap 2
6	GND
5	(+) Input 2
4	(-) Input 2
3	SVRR
2	(-) Input 1
1	(+) Input 1



NOTE: Tab connected to Pin6