

PROTECTION PRODUCTS

Description

The STF701 is a low pass T-filter with integrated TVS diodes. It is designed to provide bi-directional filtering of unwanted EMI/RFI signals and electrostatic discharge (ESD) protection in portable electronic equipment. Each device will provide filtering and protection for two I/O lines.

The STF701 is constructed using thin-film-on-silicon technology. The device has very low insertion loss in the pass band (to approximately 10MHz) and good attenuation at high frequencies (approximately 100MHz to 1GHz). Each line features two stages of TVS diode protection. The TVS diodes provide effective suppression of ESD voltages in excess of $\pm 15\text{kV}$ (air discharge) and $\pm 8\text{kV}$ (contact discharge) per IEC 61000-4-2, level 4.

The clamping characteristics of the device are optimized by the use of two TVS diodes. The TVS diodes serve a dual purpose of protecting the internal capacitor and the sensitive IC connected to the line. The voltage divider action of the circuit means the voltage across the protected IC will be very close to the breakdown voltage (V_{BR}) of the second TVS.

The small size and integrated feature of the STF701 minimizes required board space and increases system reliability. The STF701 is suitable for use in portable communications devices such as cellular handsets and accessory kits.

Features

- ◆ Bidirectional EMI/RFI filtering with integrated ESD protection
- ◆ ESD protection to **IEC 61000-4-2 (ESD) $\pm 15\text{kV}$ (air), $\pm 8\text{kV}$ (contact)**
IEC 61000-4-4 (EFT) 40A (5/50ns)
- ◆ Filtering and ESD protection for two data lines
- ◆ Low insertion loss to 10MHz
- ◆ Good attenuation of high frequency signals
- ◆ Low TVS operating voltage (5V)
- ◆ Low clamping voltage
- ◆ Low leakage current
- ◆ Thin-film-on-silicon technology

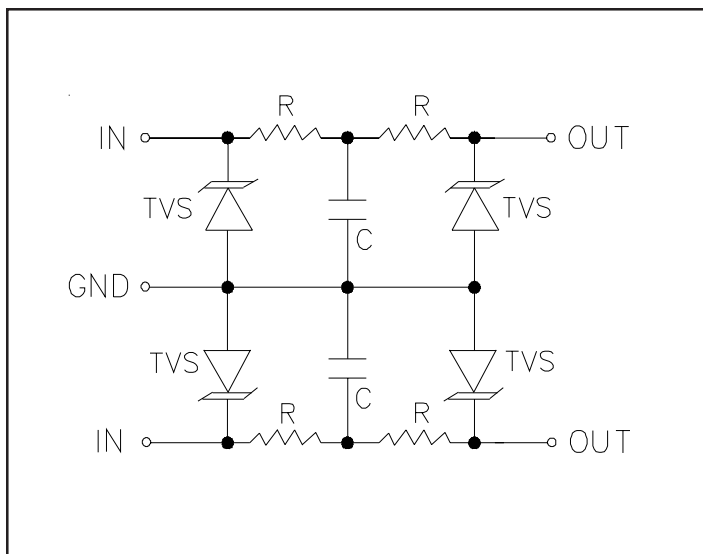
Mechanical Characteristics

- ◆ EIAJ SC70-5L package
- ◆ Molding compound flammability rating: UL 94V-0
- ◆ Marking : 701
- ◆ Packaging : Tape and Reel per EIA 481
- ◆ RoHS/WEEE Compliant

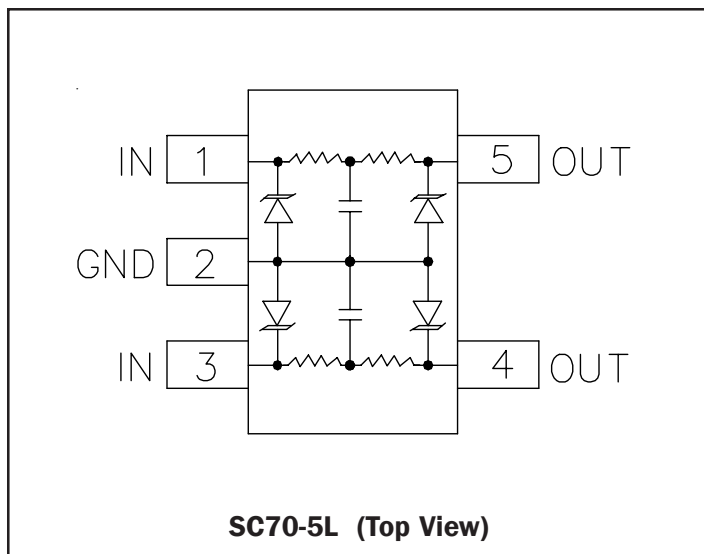
Applications

- ◆ Cell phone handsets
- ◆ Cell phone accessories
- ◆ RF Communications equipment
- ◆ Laptop Computers
- ◆ Two-way pagers
- ◆ GPS Devices

Circuit Diagram



Schematic & PIN Configuration



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Absolute Maximum Rating

Rating	Symbol	Value	Units
Steady-State Power	P_{SS}	100	mW
ESD Air Discharge per IEC 61000-4-2 ESD Contact Discharge per IEC 61000-4-2	V_{ESD}	15 8	kV
Lead Soldering Temperature	T_L	260 (10 sec.)	°C
Operating Temperature	T_J	-20 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

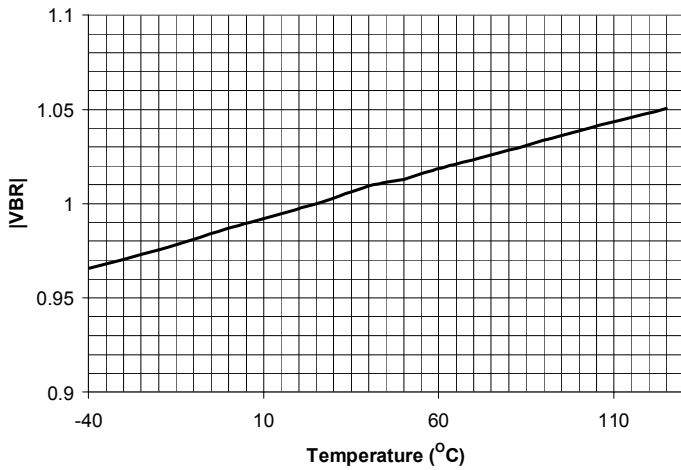
Electrical Characteristics

STF701						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
TVS Reverse Stand-Off Voltage	V_{RWM}				5	V
TVS Reverse Breakdown Voltage	V_{BR}	$I_t = 1mA$	6			V
TVS Reverse Leakage Current	I_R	$V_{RWM} = 5V, T=25^\circ C$			5	μA
TVS Reverse Leakage Current	I_R	$V_{RWM} = 3.3V, T=25^\circ C$			1	μA
TVS Junction Capacitance	C_j	Between I/O pins and Ground, each device $V_R = 0V, f = 1MHz$		65		pF
Total Series Resistance	R	Each Line	45	50	55	Ω
Capacitor	C	Each Line		100		pF
Total Capacitance	C_{TOT}	Between Input and Output, each Line $V_R = 0V, f = 1MHz$		230		pF

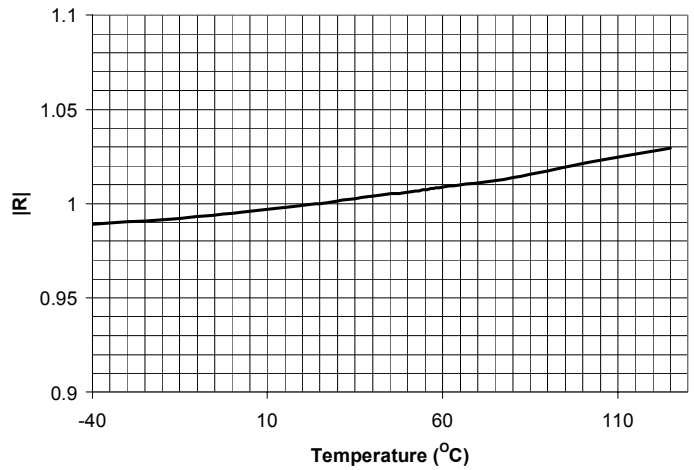
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Typical Characteristics

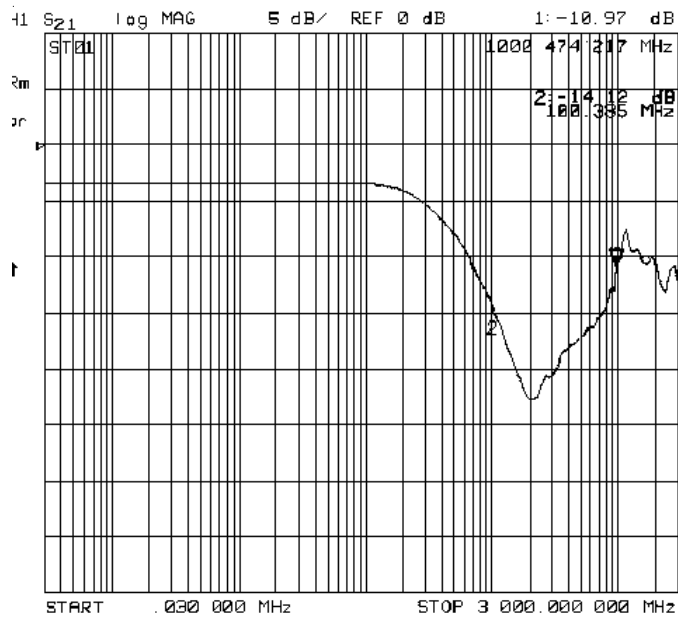
Normalized Breakdown Voltage vs. Temperature



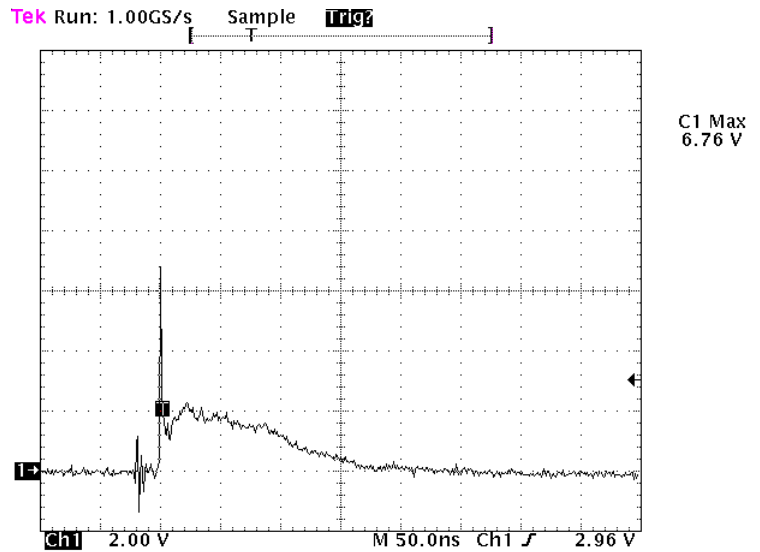
Normalized Resistance vs. Temperature



Typical Insertion Loss



**ESD Clamping
(8kV Contact per IEC 61000-4-2)**



Frequency	Attenuation
100MHz	-10.75dB
1GHz	-14.71dB

Measurement taken with Hewlett Packard Model 8753E Network Analyzer

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Applications Information

Device Connection for Protection of Two Data Lines

The STF701 is designed to provide EMI filtering and ESD protection for two I/O lines. The equivalent circuit diagram is shown in Figure 1. The device is connected as follows:

1. Line 1 is connected at pins 1 & 5 and line 2 is connected at pins 3 & 4 (Figure 2). The device is symmetrical so input & output connections can be made on either side of the device. Pin 2 is connected to ground. The ground connection should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.

Voltage Clamping Characteristics.

The clamping characteristics of the STF701 are optimized by the use of two TVS diodes in the protection circuit (Figure 3). An ESD strike on the protected line will be initially suppressed by the first TVS diode. The voltage across the TVS will be the clamping voltage of the device (V_{c1}) given by:

$$V_{c1} = V_{br} + R_D * I_{pp}$$

where

V_{br} = Breakdown voltage of the TVS

R_D = Dynamic resistance of the TVS

I_{pp} = Peak pulse (ESD) current

The dynamic resistance of the TVS is very small, typically $< 0.5\Omega$.

The second TVS will be subjected to V_{c1} through the voltage divider formed by the series resistor (R) and the dynamic resistance of the TVS. Since $R \gg R_D$ then by the voltage divider theorem, the voltage seen by the protected IC will be a few millivolts above the breakdown voltage (V_{br}) of the second TVS.

Figure 1 - STF701 Circuit Diagram

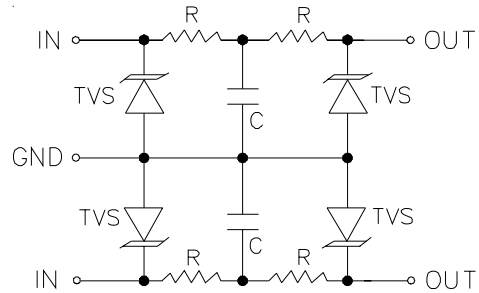


Figure 2 - STF701 Connection Diagram

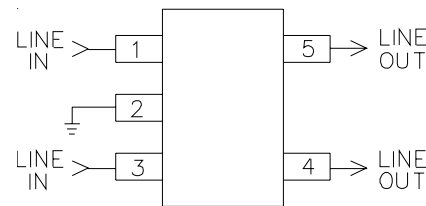
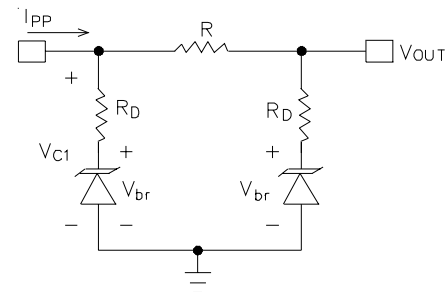


Figure 3 - STF701 Clamping Characteristics



Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

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Marking Codes

Part Number	Marking Code
STF701	701

Ordering Information

Part Number	Lead Finish	Qty per Reel	Reel Size
STF701.TC	SnPb	3,000	7 Inch
STF701.TCT	Pb Free	3,000	7 Inch

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