

Microchip**Filter specification****TFS 37B****1/5****Measurement condition**

Ambient temperature:	25	°C
Input power level:	0	dBm
Terminating impedance: *		
Input:	405 Ω	-15,4 pF
Output:	465 Ω	-8,7 pF

Characteristics

Remark:

The reference level for the relative attenuation a_{rel} of the TFS 37B is the minimum of the pass band attenuation. This value is defined as the insertion loss a_e . The nominal frequency f_N is fixed at 37,8 MHz without any tolerance. The values of relative attenuation a_{rel} are guaranteed for the whole operating temperature range. The frequency shift of the filter in the operating temperature range is included in the production tolerance scheme.

D a t a		typ. value		tolerance / limit		
Insertion loss (reference level)		a_e	17,5 dB	max.	20,0	dB
Nominal frequency		f_N	-		37,8	MHz
Passband		PB	-	$f_N \pm$	3,8	MHz
Pass band ripple (p-p)			0,6 dB	max.	1,0	dB
Bandwidth		BW				
1 dB			7,9 MHz	min.	7,6	MHz
40 dB			10,2 MHz	max.	10,6	MHz
Relative attenuation		a_{rel}				
f_N	$\dots f_N \pm 3,8$ MHz		0,8 dB	max.	1	dB
$f_N \pm 12$ MHz	$\dots f_N \pm 16$ MHz		46 dB	min.	40	dB
$f_N \pm 16$ MHz	$\dots f_N \pm 20$ MHz		50 dB	min.	45	dB
Input power level			-	max.	10	dBm
Operating temperature range		OTR	-	- 10 °C ... + 60 °C		
Storage temperature range			-	- 40 °C ... + 80 °C		
Temperature coefficient of frequency		TC_f^{**}	-72 ppm/K	-		

*) The terminating impedances depend on parasitics and q-values of matching elements and the board used, and are to be understood as reference values only. Should there be additional questions do not hesitate to ask for an application note or contact our design team.

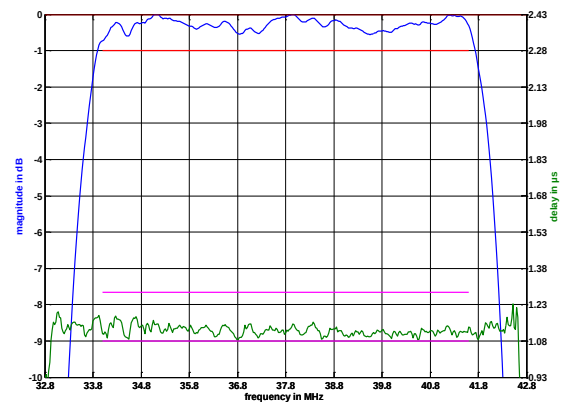
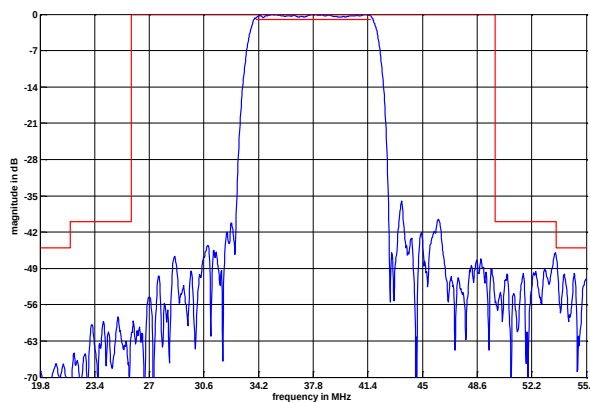
**) $\Delta f_c(\text{Hz}) = TC_f(\text{ppm/K}) \times (T - T_A) \times f_{CAT}(\text{MHz})$

Generated:

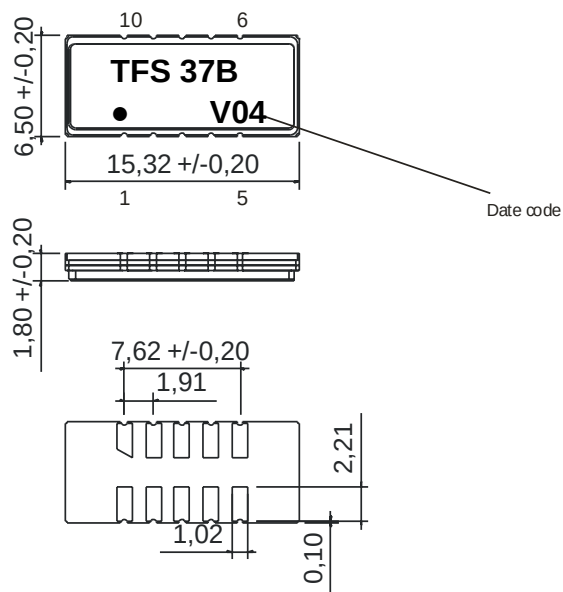
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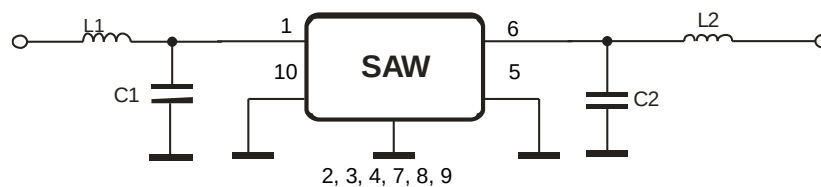
Filter characteristic**Construction and pin connection**

(All dimensions in mm)



- | | |
|----|------------------|
| 1 | Input |
| 2 | Ground |
| 3 | Ground |
| 4 | Ground |
| 5 | Output RF Return |
| 6 | Output |
| 7 | Ground |
| 8 | Ground |
| 9 | Ground |
| 10 | Input RF Return |

Date code: Year + week
 V 2007
 W 2008
 X 2009
 ...

50 Ohm Test circuit

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Stability characteristics, reliability

After the following tests the filter shall meet the whole specification:

1. Shock: 500g, 1 ms, half sine wave, 3 shocks each plane;
DIN IEC 68 T2 - 27
2. Vibration: 10 Hz to 500 Hz, 0,35 mm or 5 g respectively, 1 octave per min, 10 cycles per plan, 3 plans;
DIN IEC 68 T2 - 6
3. Change of temperature: -55 °C to 125°C / 30 min. each / 10 cycles
DIN IEC 68 part 2 – 14 Test N
4. Resistance to solder heat (reflow): reflow possible: three times max.;
for temperature conditions refer to the attached "Air reflow temperature conditions" on page 4;

This filter is RoHS compliant (2002/95/EG, 2005/618/EG)

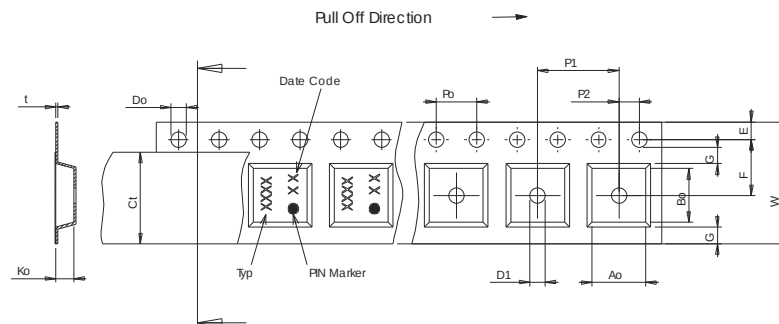
Packing

Tape & Reel: IEC 286 – 3, with exception of value for N and minimum bending radius;
tape type II, embossed carrier tape with top cover tape on the upper side;

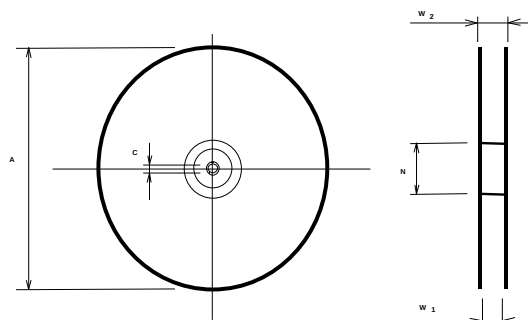
max. pieces of filters per reel:	2000
reel of empty components at start:	min. 300 mm
reel of empty components at start including leader:	min. 500 mm
trailer:	min. 300 mm

Tape (all dimensions in mm)

W	: 24,00 ± 0,3
Po	: 4,00 ± 0,1
Do	: 1,50 +0,1/-0
E	: 1,75 ± 0,1
F	: 11,50 ± 0,1
G(min)	: 0,60
P2	: 2,00 ± 0,1
P1	: 12,00 ± 0,1
D1(min)	: 1,50
Ao	: 7,10 ± 0,1
Bo	: 15,90 ± 0,1
Ct	: 21,5 ± 0,1

**Reel (all dimensions in mm)**

A	: 330
W1	: 24,4 +2/-0
W2(max)	: 30,4
N(min)	: 60
C	: 13,0 +0,5/-0,2



The minimum bending radius is 45 mm.

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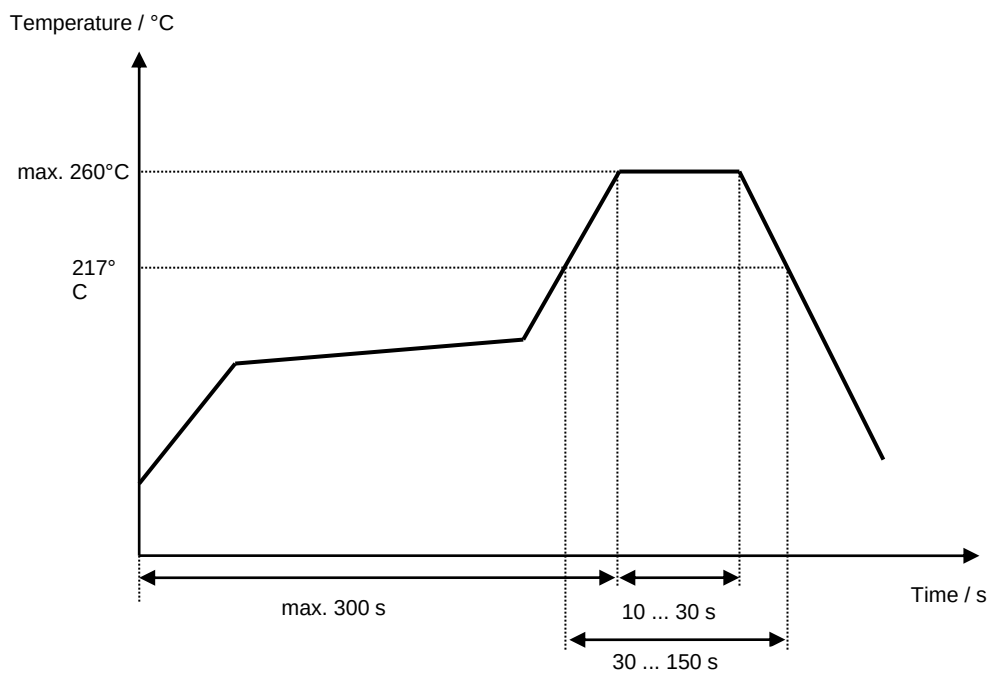
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Air reflow temperature conditions

Conditions	Exposure
Average ramp-up rate (30°C to 217°C)	less than 3°C/second
> 100°C	between 300 and 600 seconds
> 150°C	between 240 and 500 seconds
> 217°C	between 30 and 150 seconds
Peak temperature	max. 260°C
Time within 5°C of actual peak temperature	between 10 and 30 seconds
Cool-down rate (Peak to 50°C)	less than 6°C/second
Time from 30°C to Peak temperature	no greater than 300 seconds

Chip-mount air reflow profile

Microchip**Filter specification****TFS 37B****5/5****History**

Version	Reason of changes	Name	Date
1.0	- Generation of development specification	Strehl	18.09.2006
1.1	- add of terminating impedances, typical values and filter characteristics	Pfeiffer	26.01.2007