

Microchip**Filter specification****TFS900****1/5****Measurement condition**

Ambient temperature T_A :	23	°C
Input power level:	0	dBm
Terminating impedance: *)		
Input:	142 Ω	-4.5 pF
Output:	197 Ω	-3.7 pF

Characteristics

Remark:

The reference level for the relative attenuation a_{rel} of the TFS900 is the maximum attenuation in the pass band. The maximum attenuation in the pass band is defined as the insertion loss a_e . The nominal frequency f_N is fixed at 900 MHz without any tolerance or limit. The values of relative attenuation a_{rel} are guaranteed for the whole operating temperature range. The frequency shift of the filter within the operating temperature range is included in the production tolerance scheme.

D a t a		typ. value		tolerance / limit	
Insertion loss (reference level)		$a_e = a_{min}$	15.2 dB	max.	17.0 dB
Nominal frequency		f_N	-		900 MHz
Passband		PB	-	$f_N \pm$	60 MHz
Pass band ripple			0.4 dB	max.	1 dB
Relative attenuation		a_{rel}			
1 MHz	... 740 MHz		44 dB	min.	30 dB
740 MHz	... 760 MHz		43 dB	min.	25 dB
1040 MHz	... 1060 MHz		39 dB	min.	25 dB
1060 MHz	... 2000 MHz		42 dB	min.	30 dB
Group delay ripple in PB			3.5 ns		-
Input power level				max.	5 dBm
Operating temperature range		OTR	-		-40 °C ... +91 °C
Storage temperature range			-		-55 °C ... +125 °C
Temperature coefficient of frequency		$TC_f^{**})$	-96 ppm/k		

*) The terminating impedances depend on parasitics and q-values of matching elements and the board used, and are to be understood as reference values only. Should there be additional questions do not hesitate to ask for an application note or contact our design team.

***) $\Delta f = TC_f(T - T_0)f_N$

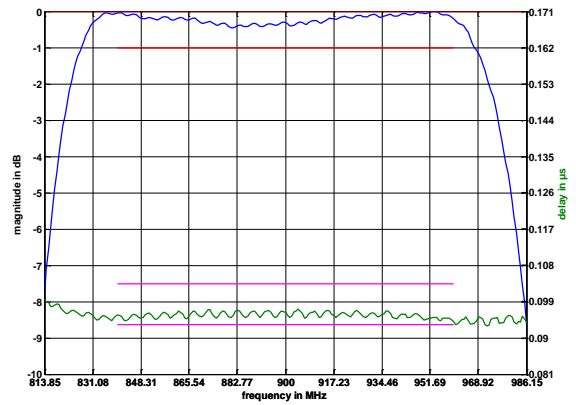
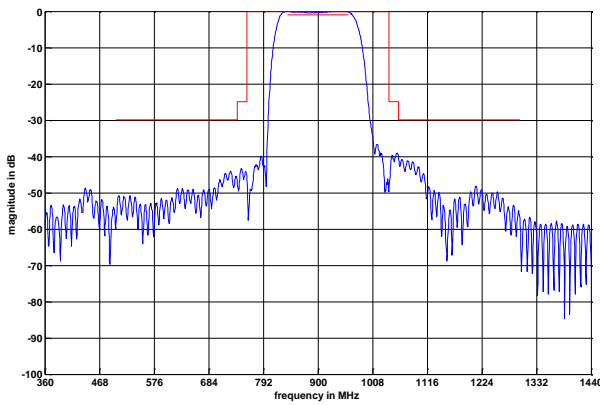
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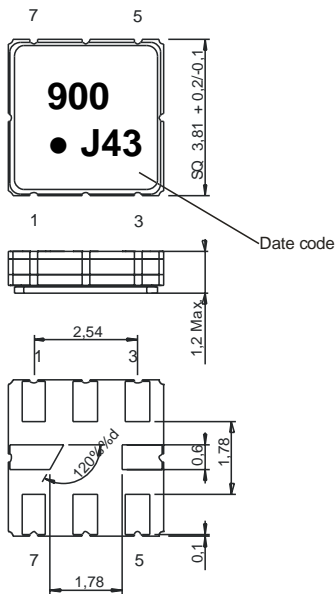
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Filter characteristic



Construction and pin connection

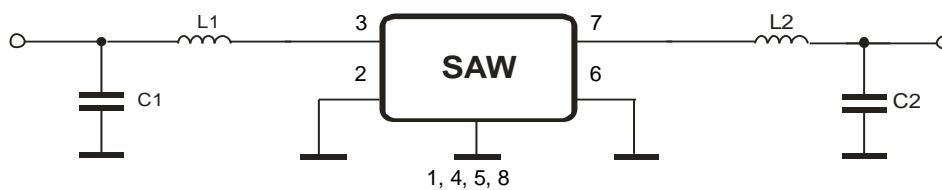
(All dimensions in mm)



- 1 Ground
- 2 Ground
- 3 Input
- 4 Ground
- 5 Ground
- 6 Ground
- 7 Output
- 8 Ground

Date code: Year + week
 J 2017
 K 2018
 L 2019
 ...

50 Ω Test circuit



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Stability characteristics, reliability

After the following tests the filter shall meet the whole specification:

1. Shock: 500 g, 1 ms, half sine wave, 3 shocks each plane;
DIN IEC 60068 T2 - 27
2. Vibration: 10 Hz to 2000 Hz, 0.35 mm or 5 g respectively, 1 octave per min, 10 cycles per plane, 3 planes; DIN IEC 60068 T2 - 6
3. Change of temperature: -55 °C to 125 °C / 15 min. each / 100 cycles
DIN IEC 60068 part 2 – 14 Test N
4. Resistance to solder heat (reflow): reflow possible: three times max.;
for temperature conditions refer to the attached "Air reflow temperature conditions" on page 4;
5. SAW devices are Electrostatic Discharge (ESD) sensitive devices.

This filter is RoHS compliant (2011/65/EU)

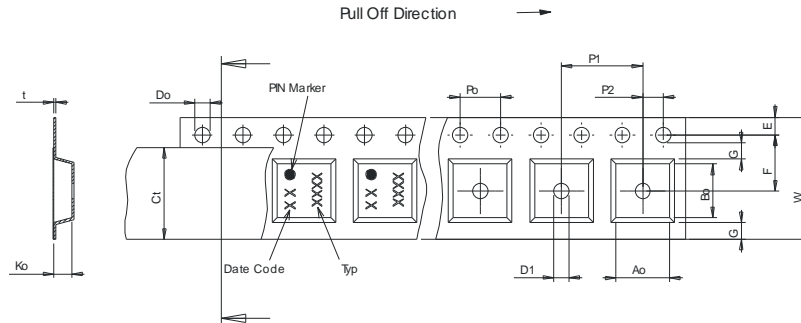
Packing

Tape & Reel: IEC 286 – 3, with exception of value for N and minimum bending radius;
tape type II, embossed carrier tape with top cover tape on the upper side;

reel of empty components at start:	min. 300 mm
reel of empty components at start including leader:	min. 500 mm
trailer:	min. 300 mm

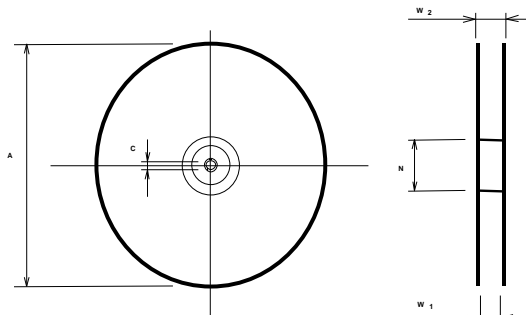
Tape (all dimensions in mm)

- W : 12.00 ±0.3
- Po : 4.00 ±0.1
- Do : 1.50 +0.1/-0
- E : 1.75 ±0.1
- F : 5.50 ±0.05
- G(min) : 0.75
- P2 : 2.00 ±0.05
- P1 : 8.00 ±0.1
- D1(min) : 1.50
- Ao : 4.30 ±0.1
- Bo : 4.30 ±0.1
- Ct : 9.2 ±0.1
- Ko : 1.80 ±0.1
- t : 0.30 ±0.05



Reel (all dimensions in mm)

- A : 330 or 180
- W1 : 12.4 +2/-0
- W2(max) : 18.40
- N(min) : 50.00
- C : 13.0 +0.5/-0.2



The minimum bending radius is 45 mm.

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Air reflow temperature conditions

Conditions	Exposure
Average ramp-up rate (30 °C to 217 °C)	less than 3 °C / second
> 100 °C	between 300 and 600 seconds
> 150 °C	between 240 and 500 seconds
> 217 °C	between 30 and 150 seconds
Peak temperature	max. 260 °C
Time within 5 °C of actual peak temperature	between 10 and 30 seconds
Cool-down rate (Peak to 50 °C)	less than 6 °C / second
Time from 30 °C to Peak temperature	no greater than 300 seconds

Chip-mount air reflow profile



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History

Version	Reason of Changes	Name	Date
1.0	- Generation of development specification	Chilla	13.12.2005
1.1	- generated filter specification - added terminating impedances - added typical values - added filter characteristic - added test circuit	Chilla	18.05.2006
2.0	- extend OTR	Chilla	30.06.2011
2.1	- updated header and footer	Raura	19.07.2011
2.2	- Update storage temperature range	P. Jaster	21.09.2017
3.0	- changed input power level - changed operating temperature range	P. Jaster	23.10.2017